

6658239

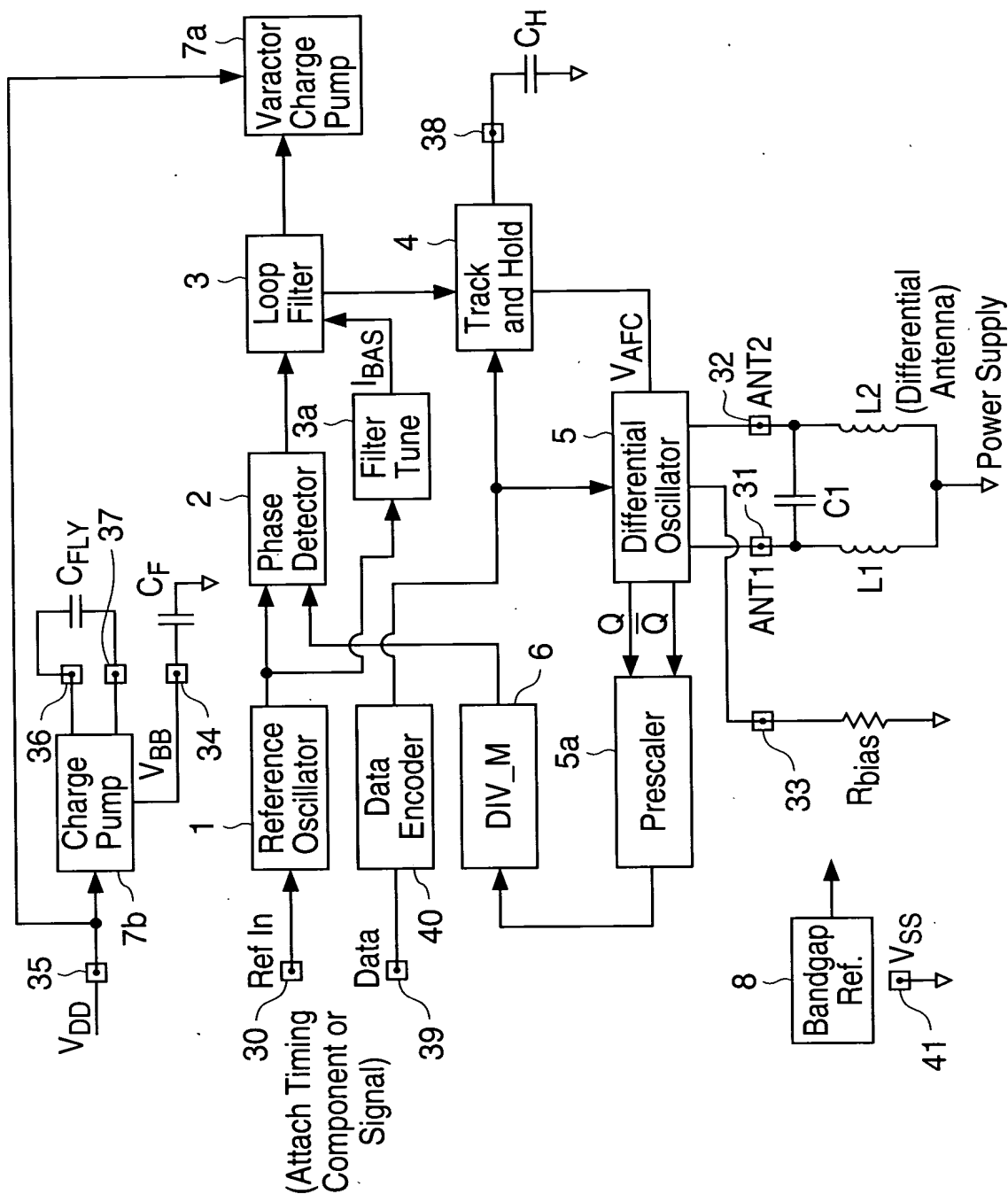


FIG. 1a

(34)

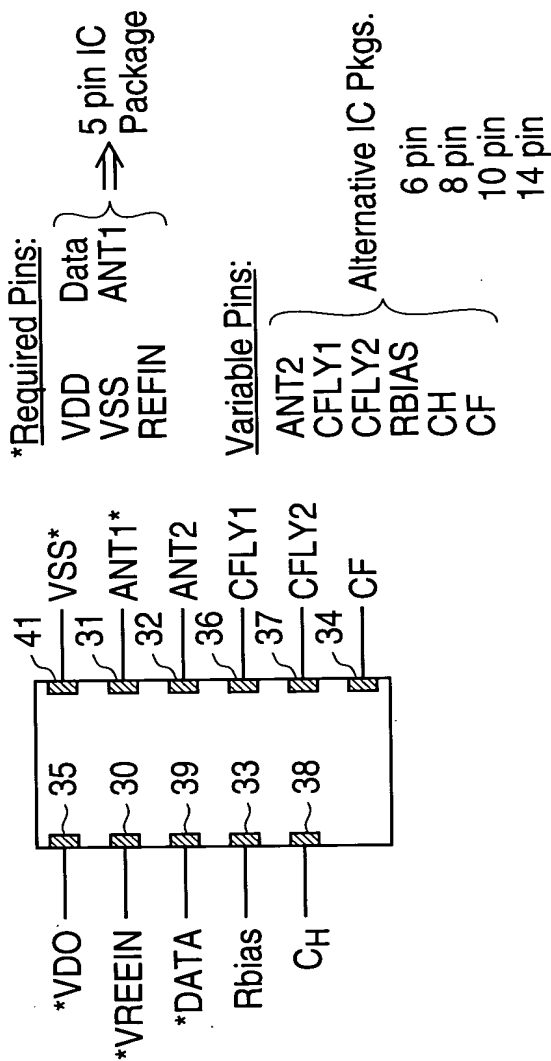


FIG. 1b



FIG. 2a

CONNECTION PIN DIAGRAM FOR THE INVENTION
 FULLY INTEGRATED, ALL CMOS ASK/AM TRANSMITTER WITH AUTOMATIC ANTENNA TUNING

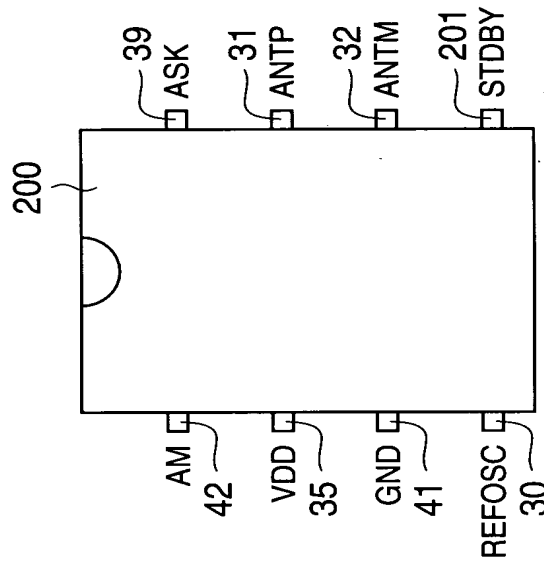


FIG. 2b

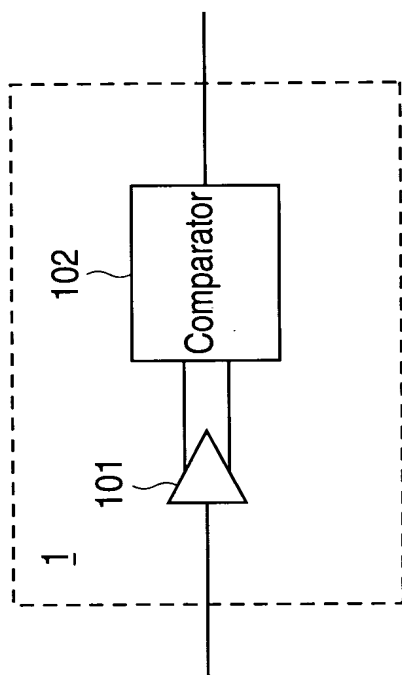


FIG. 3a

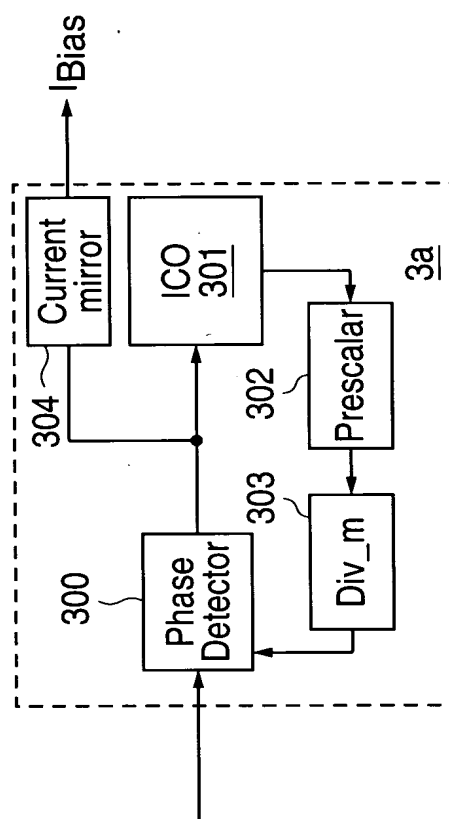


FIG. 3b

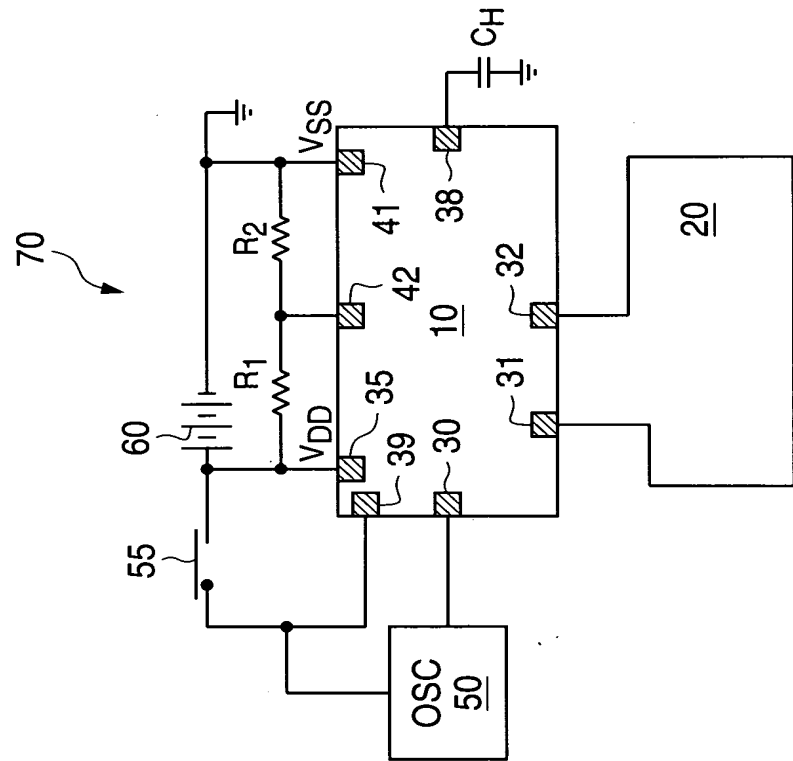


FIG. 4

AUTOMATIC ANTENNA TUNING

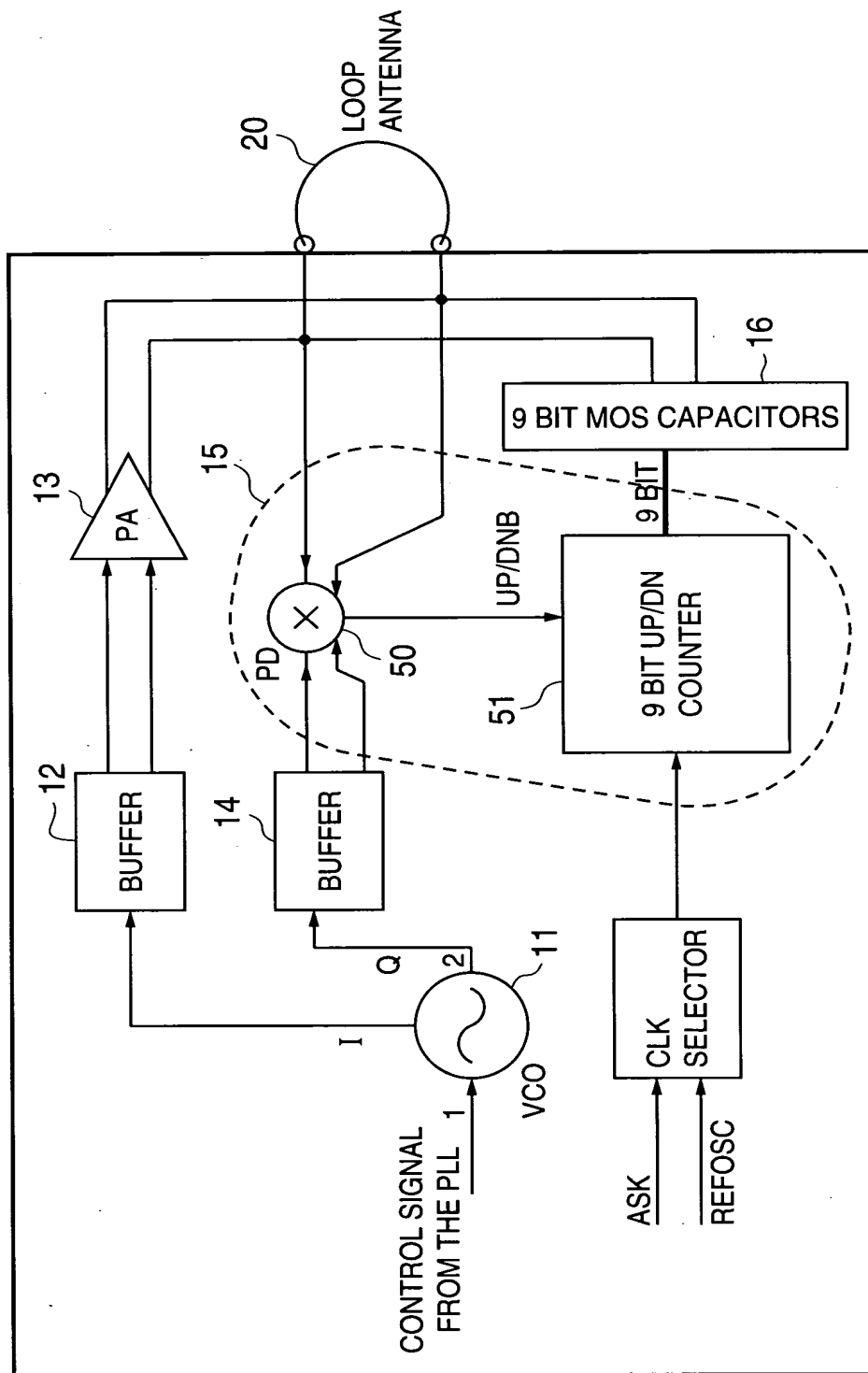


FIG. 5a

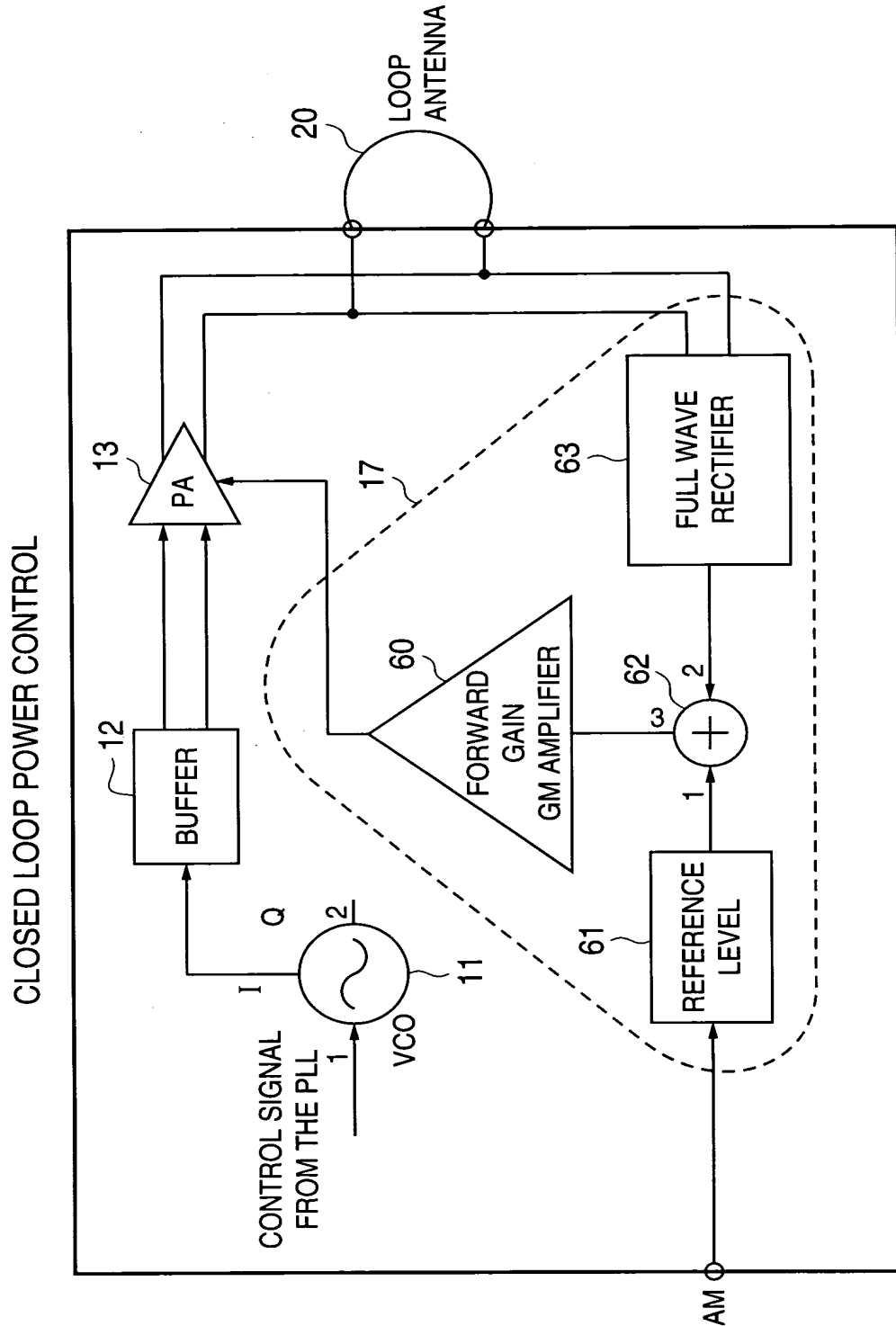
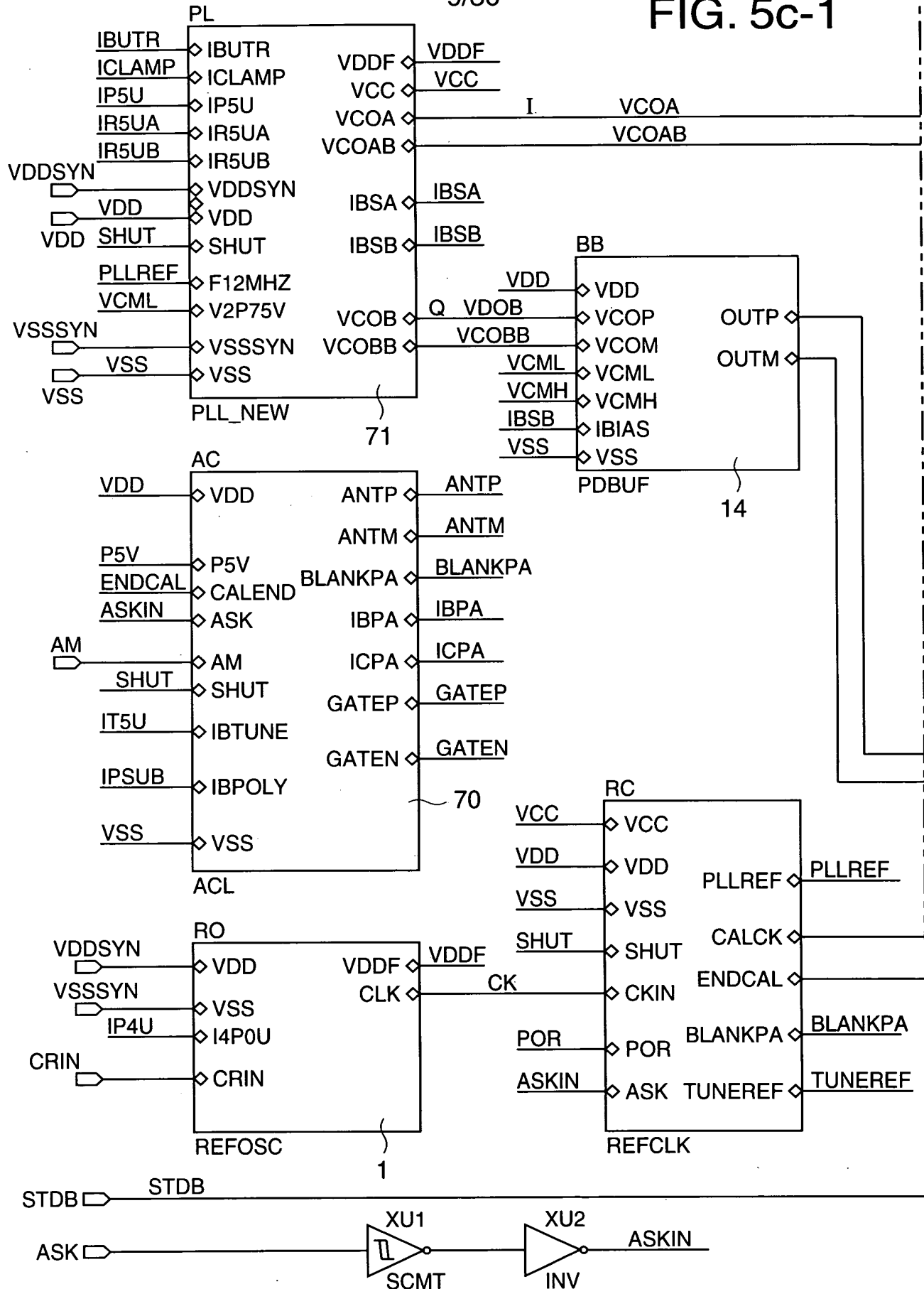


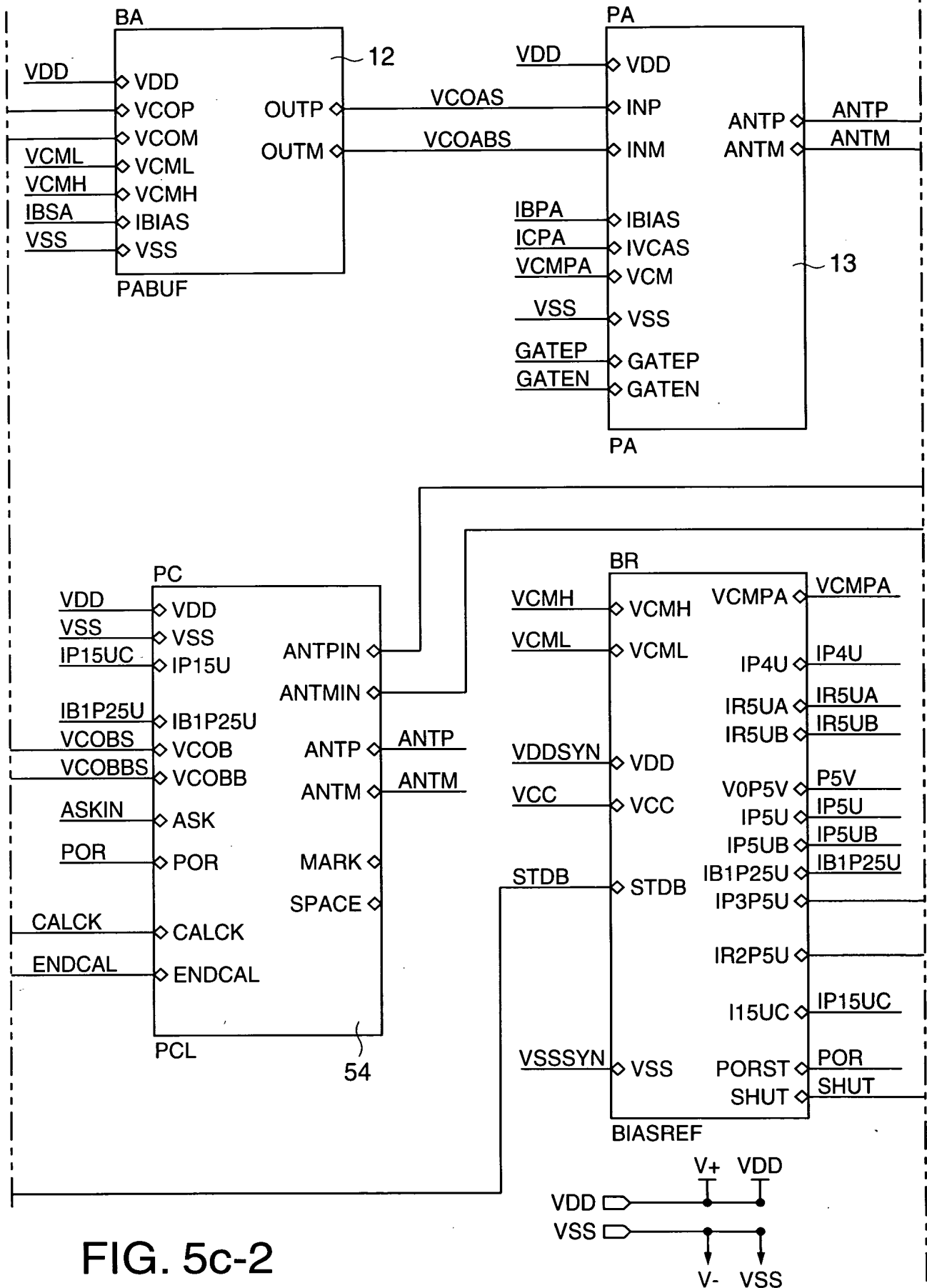
FIG. 5b

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FIG. 5c-1



0983775/5772860



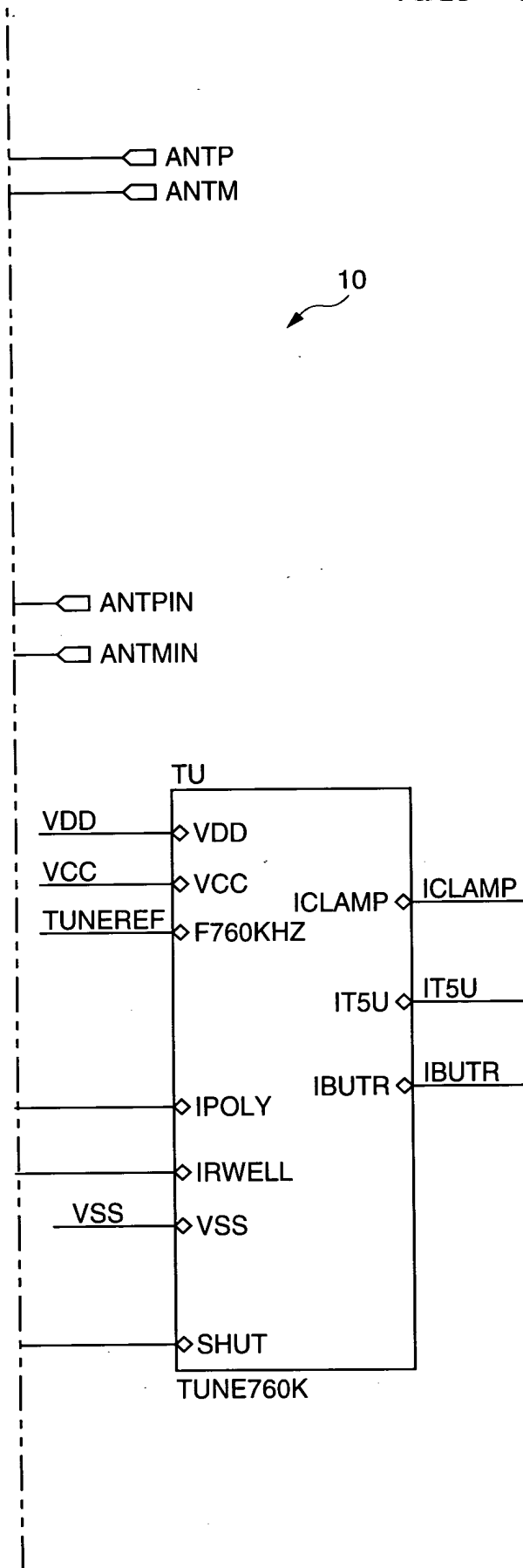


FIG. 5c-3

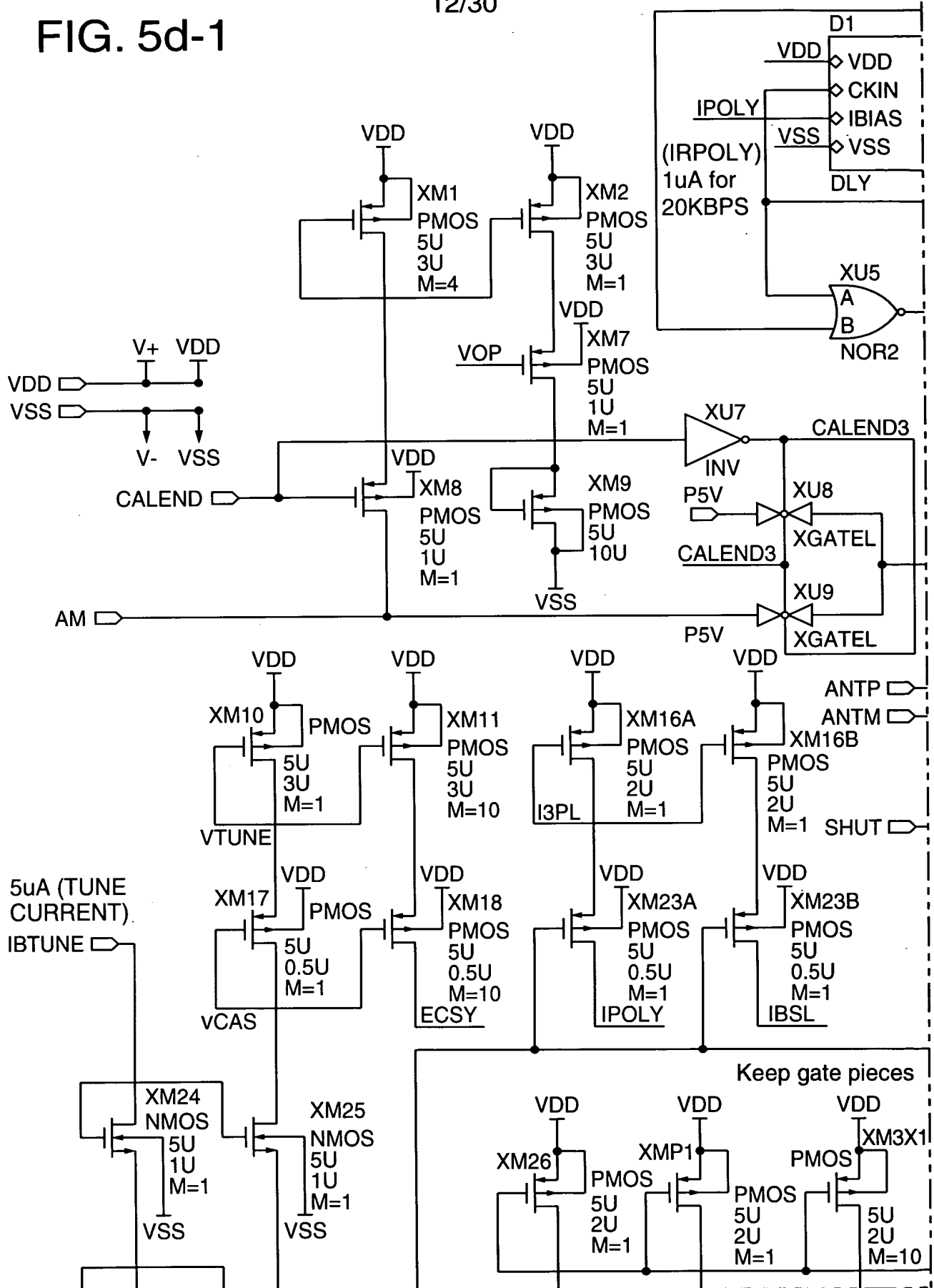
FIG. 5c-1

FIG. 5c-2

FIG. 5c-3

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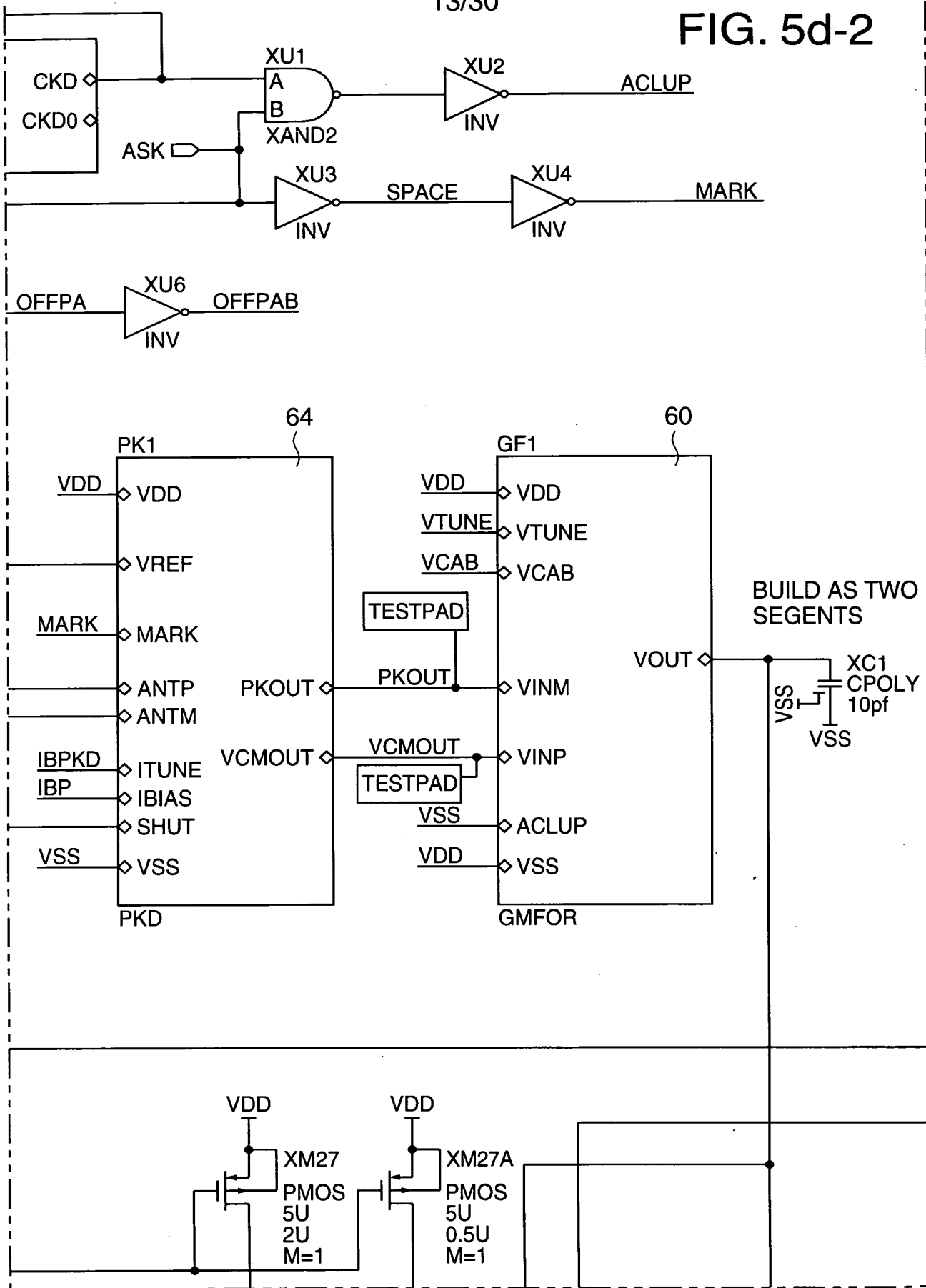
FIG. 5d-1



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FIG. 5d-2



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FIG. 5d-3

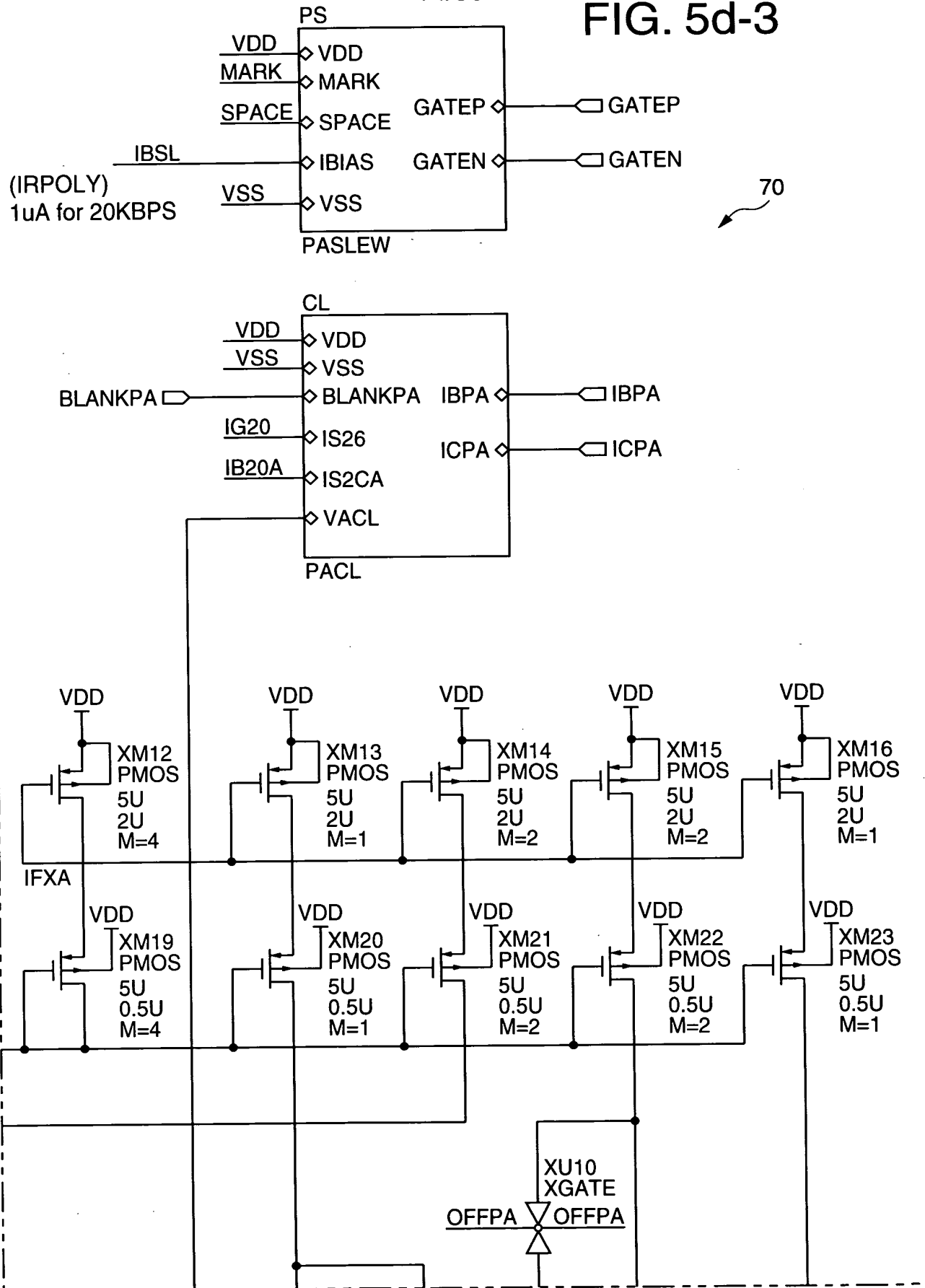


FIG. 5d-3

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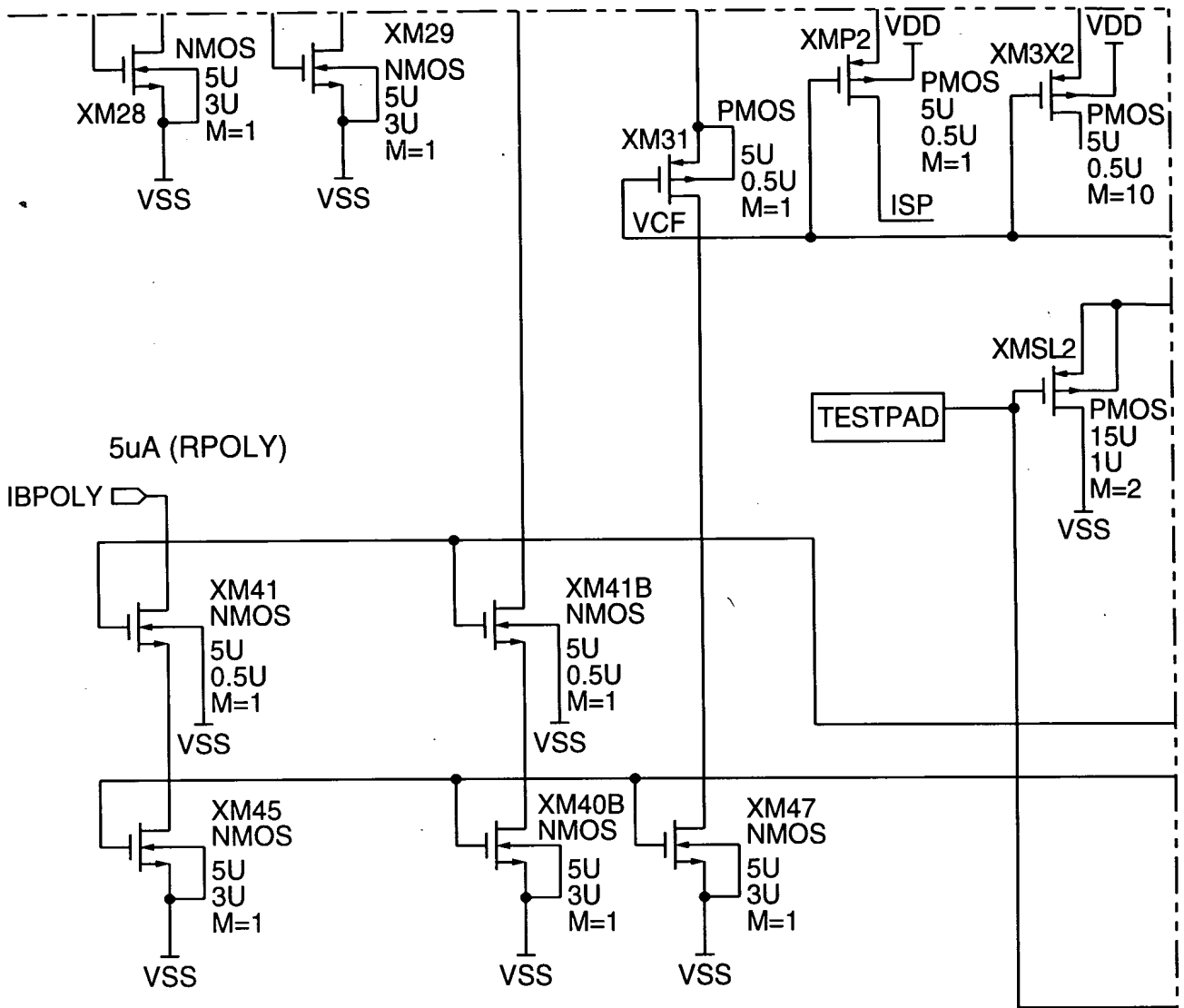


FIG. 5d-4

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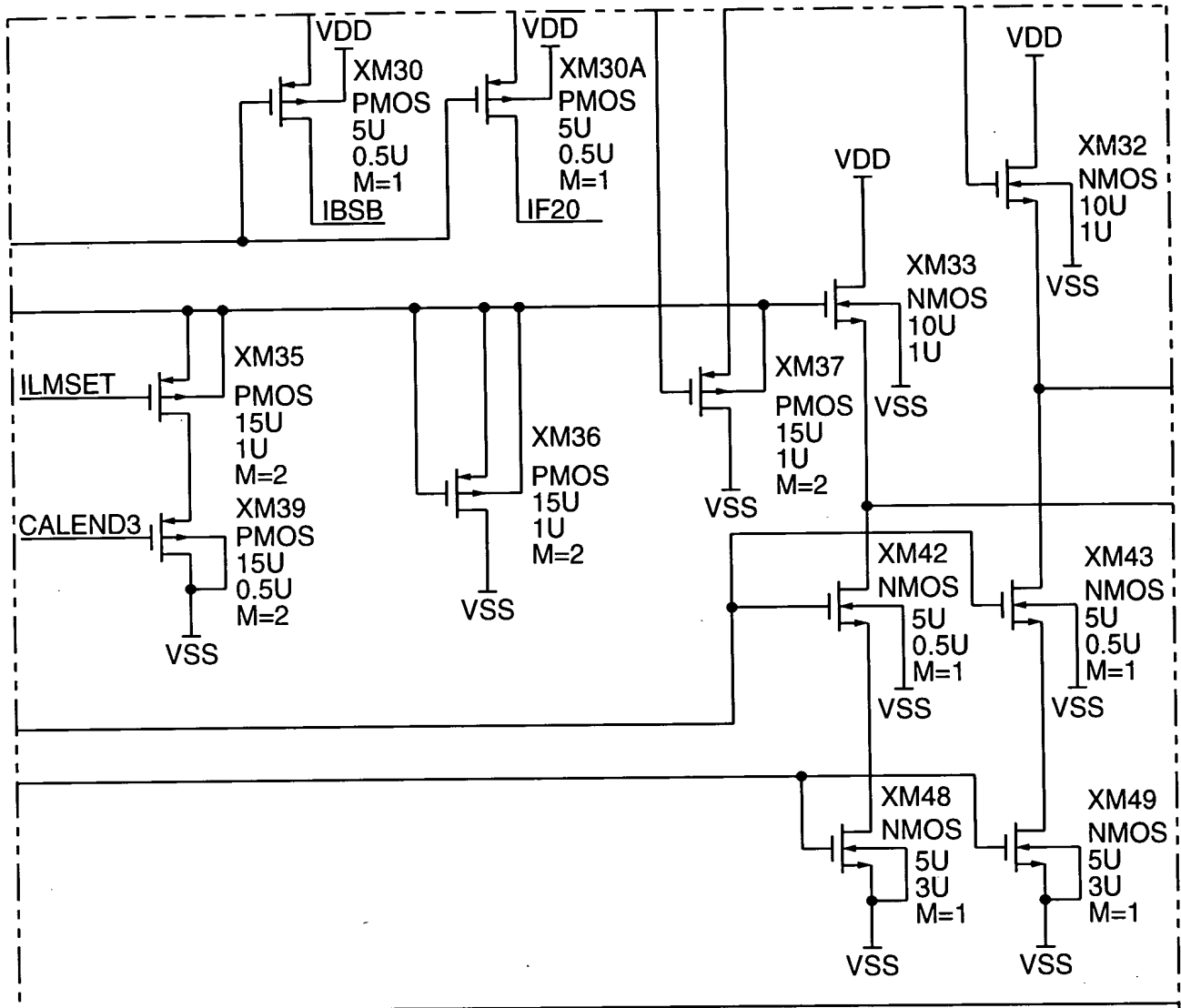


FIG. 5d-5

TOP SECRET

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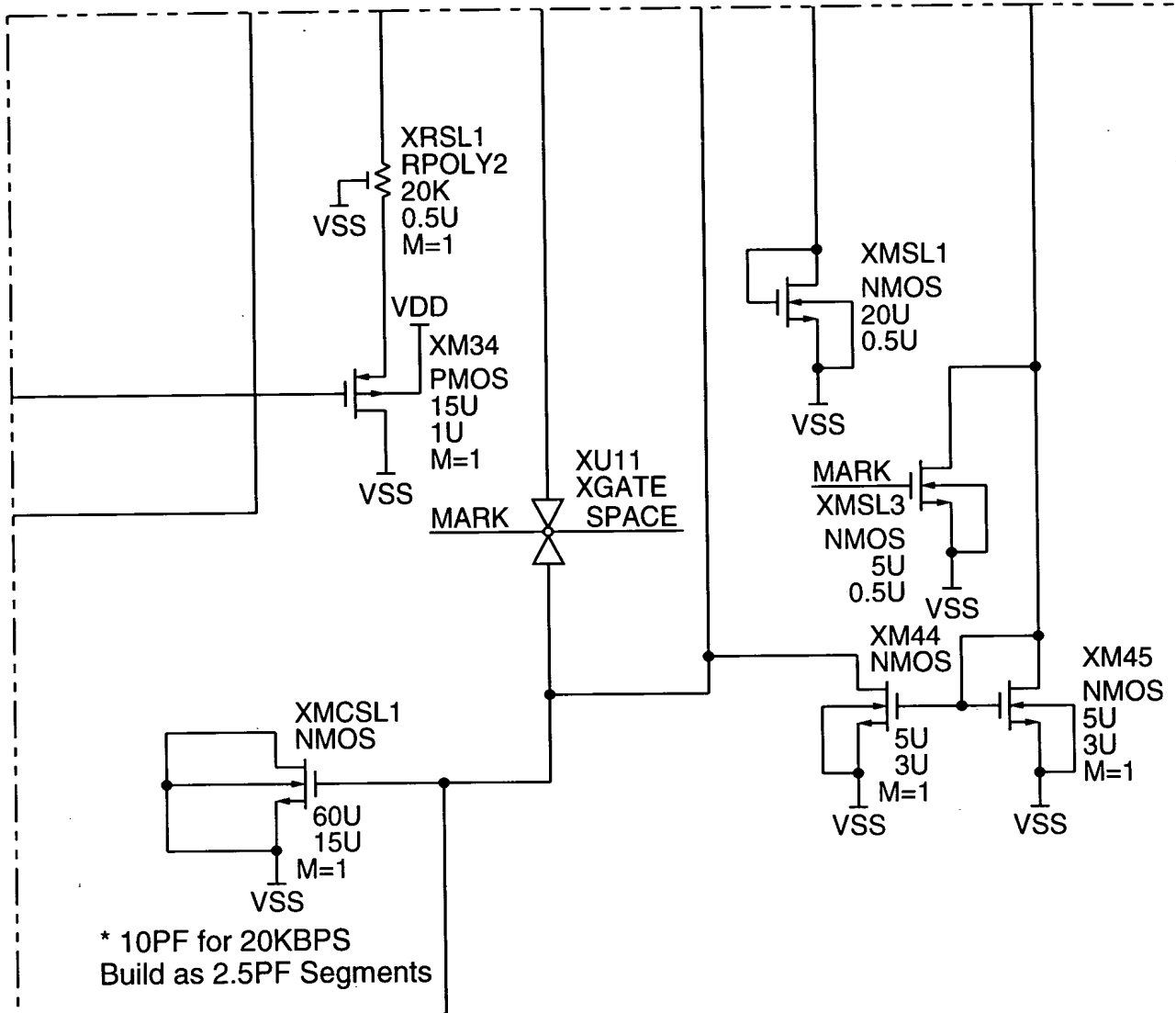


FIG. 5d-6

FIG. 5d-1	FIG. 5d-2	FIG. 5d-3
FIG. 5d-4	FIG. 5d-5	FIG. 5d-6

FIG. 5d

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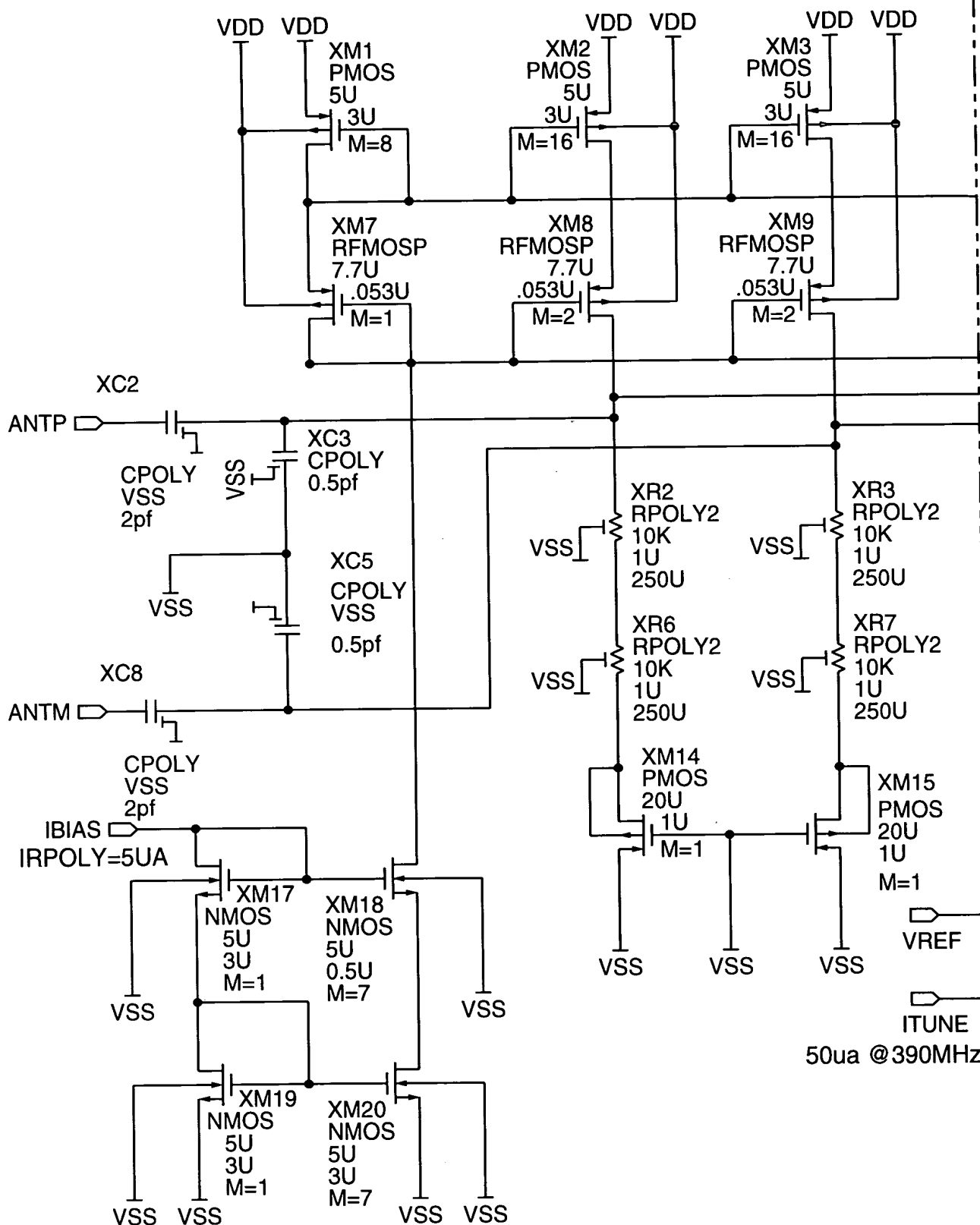
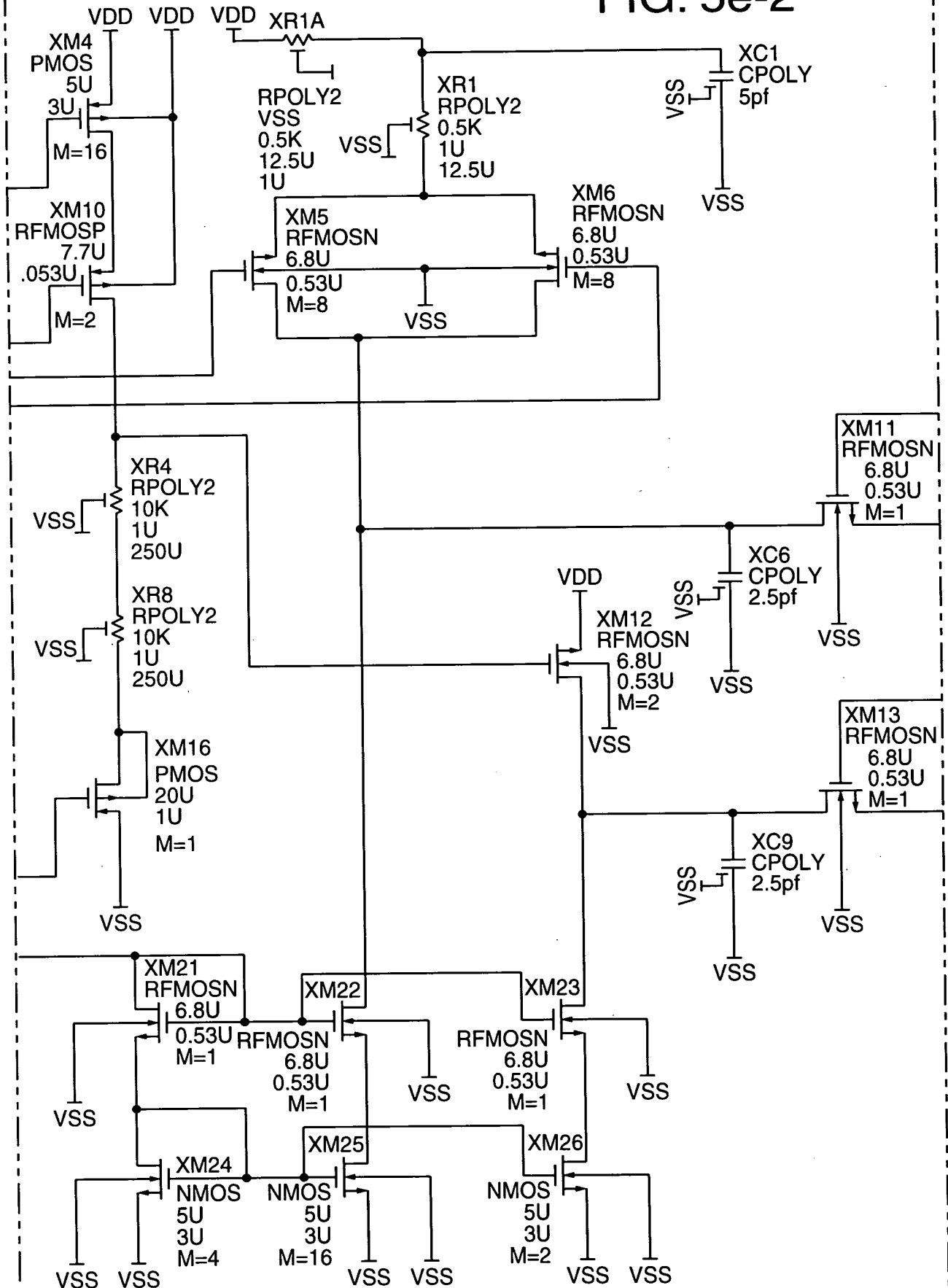


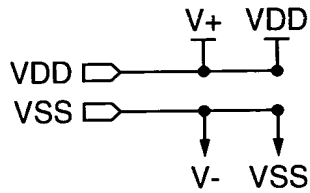
FIG. 5e-1

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FIG. 5e-2



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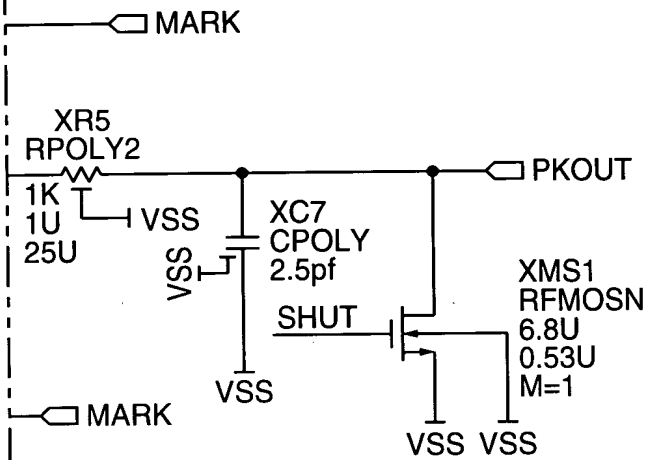


FIG. 5e-3

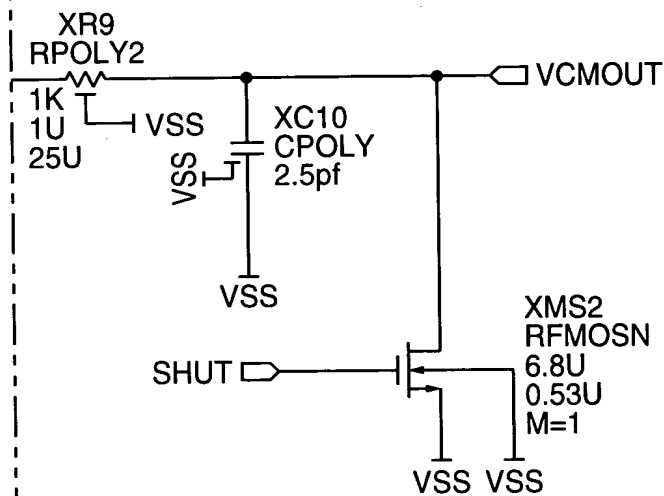


FIG. 5e-1	FIG. 5e-2	FIG. 5e-3
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FIG. 5e

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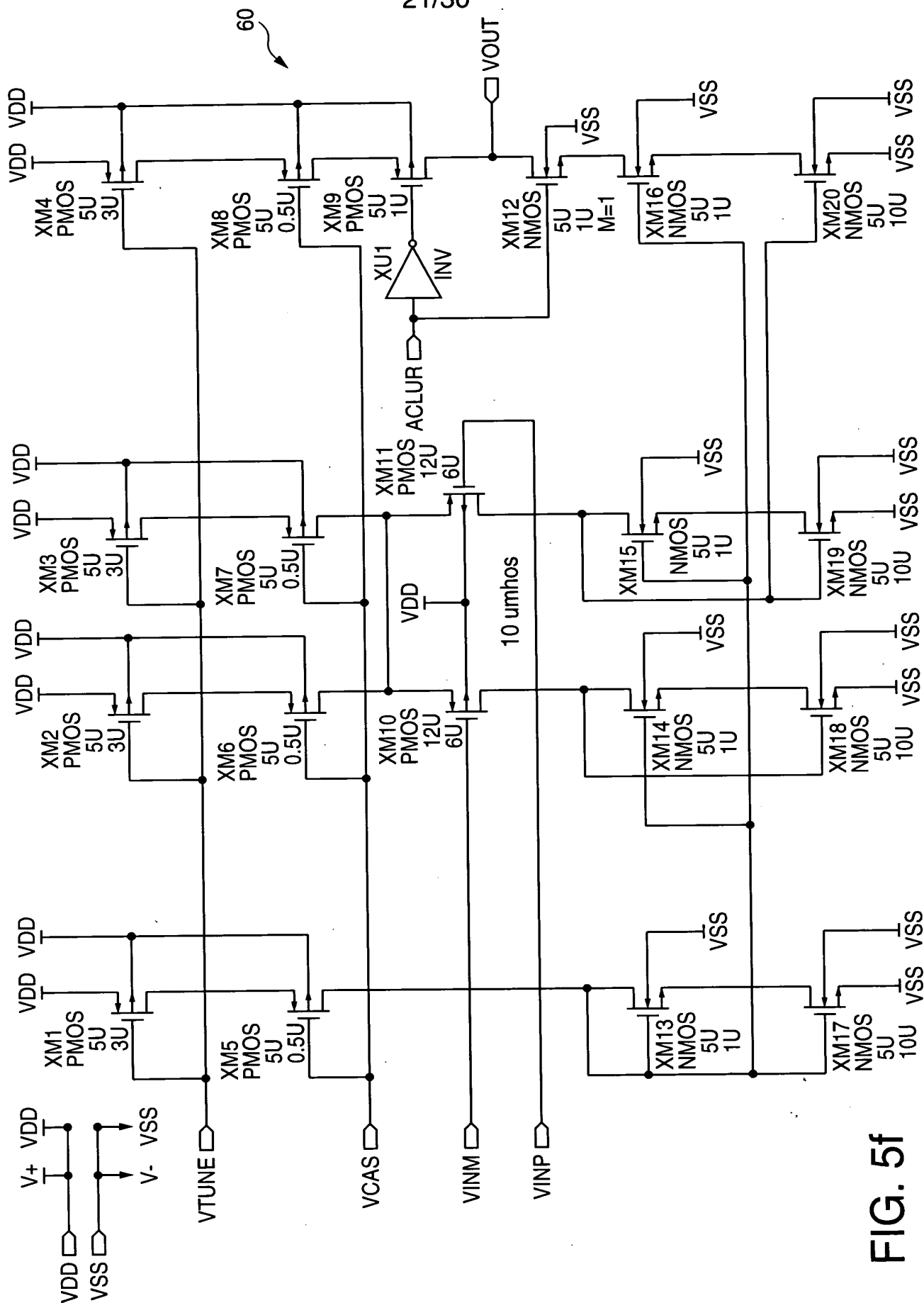
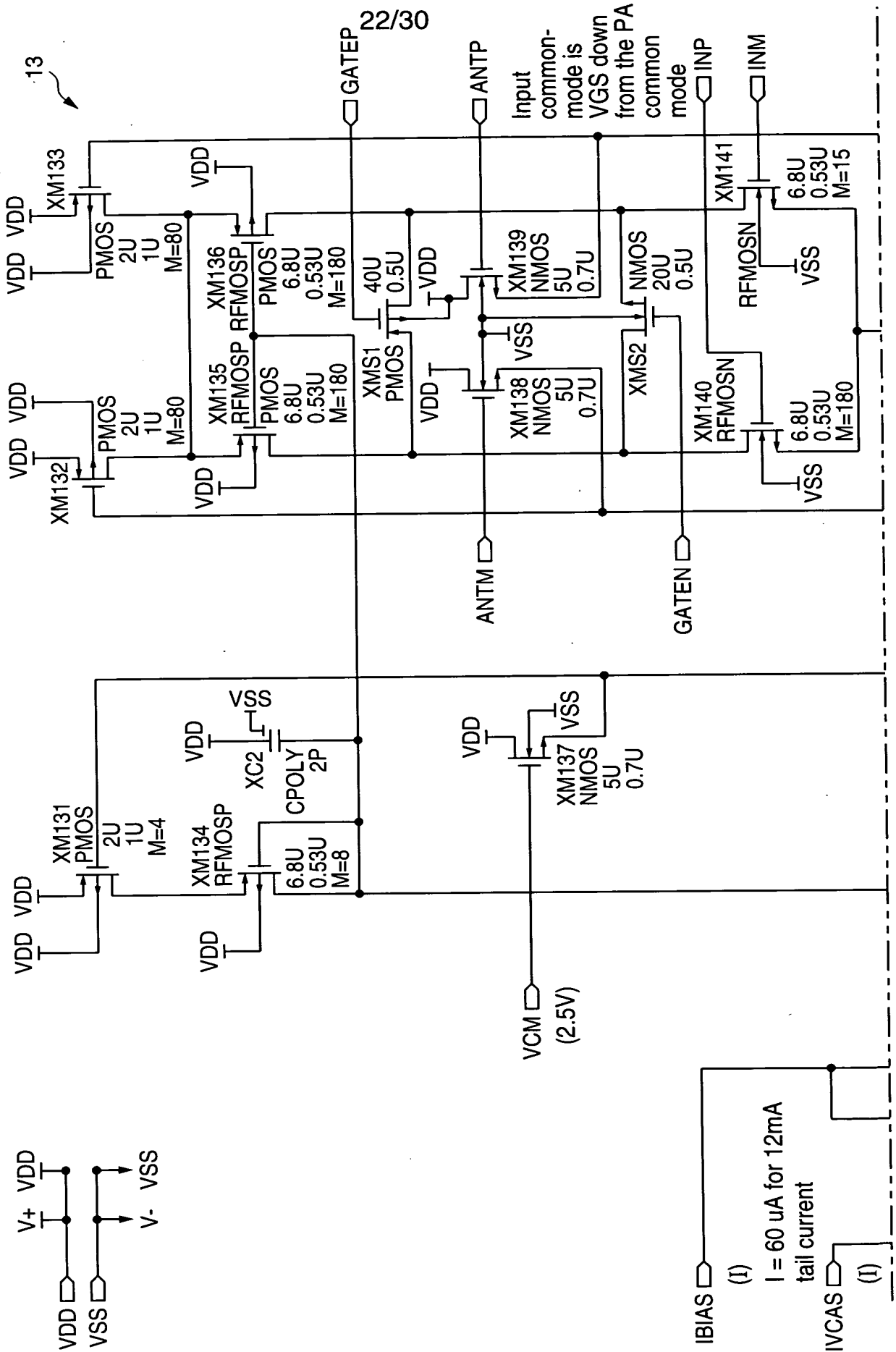


FIG. 5f

FIG. 5g-1

POWER AMPLIFIER



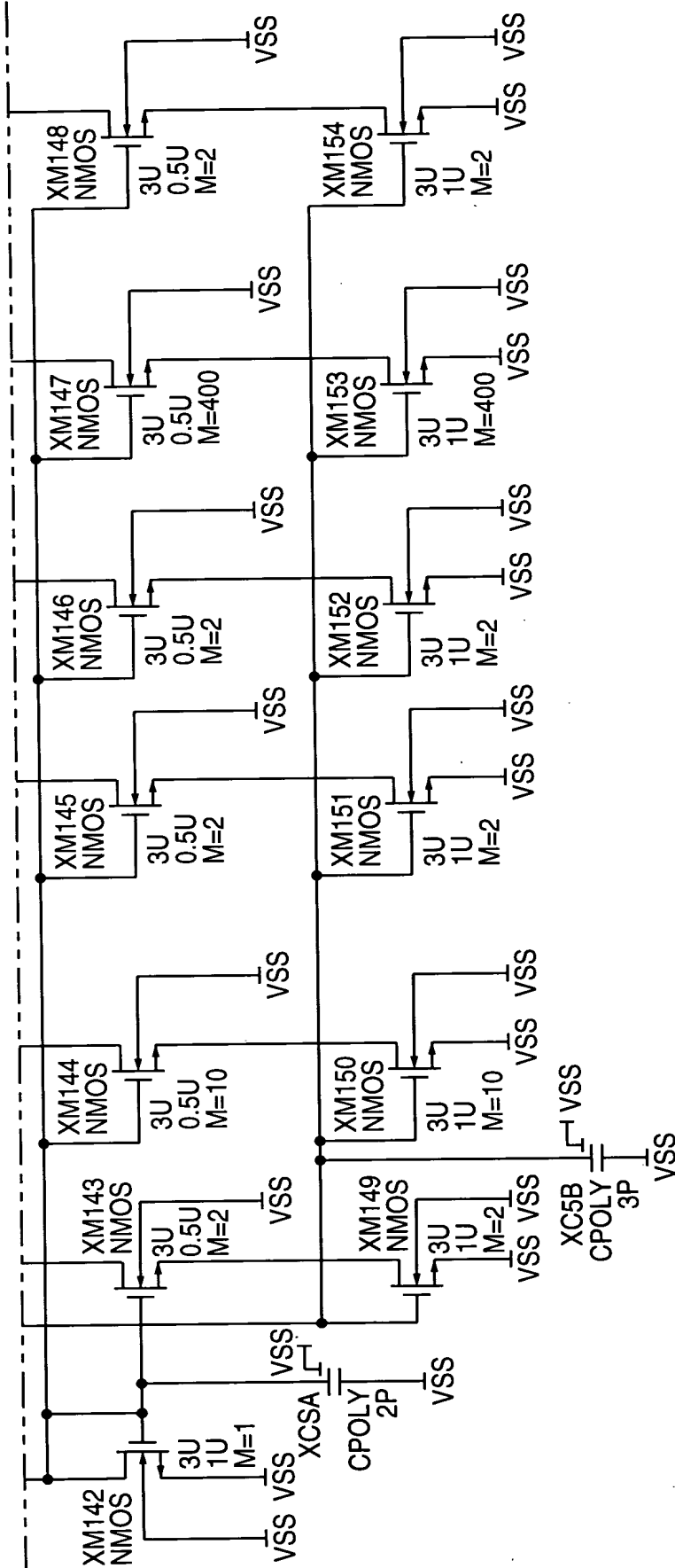
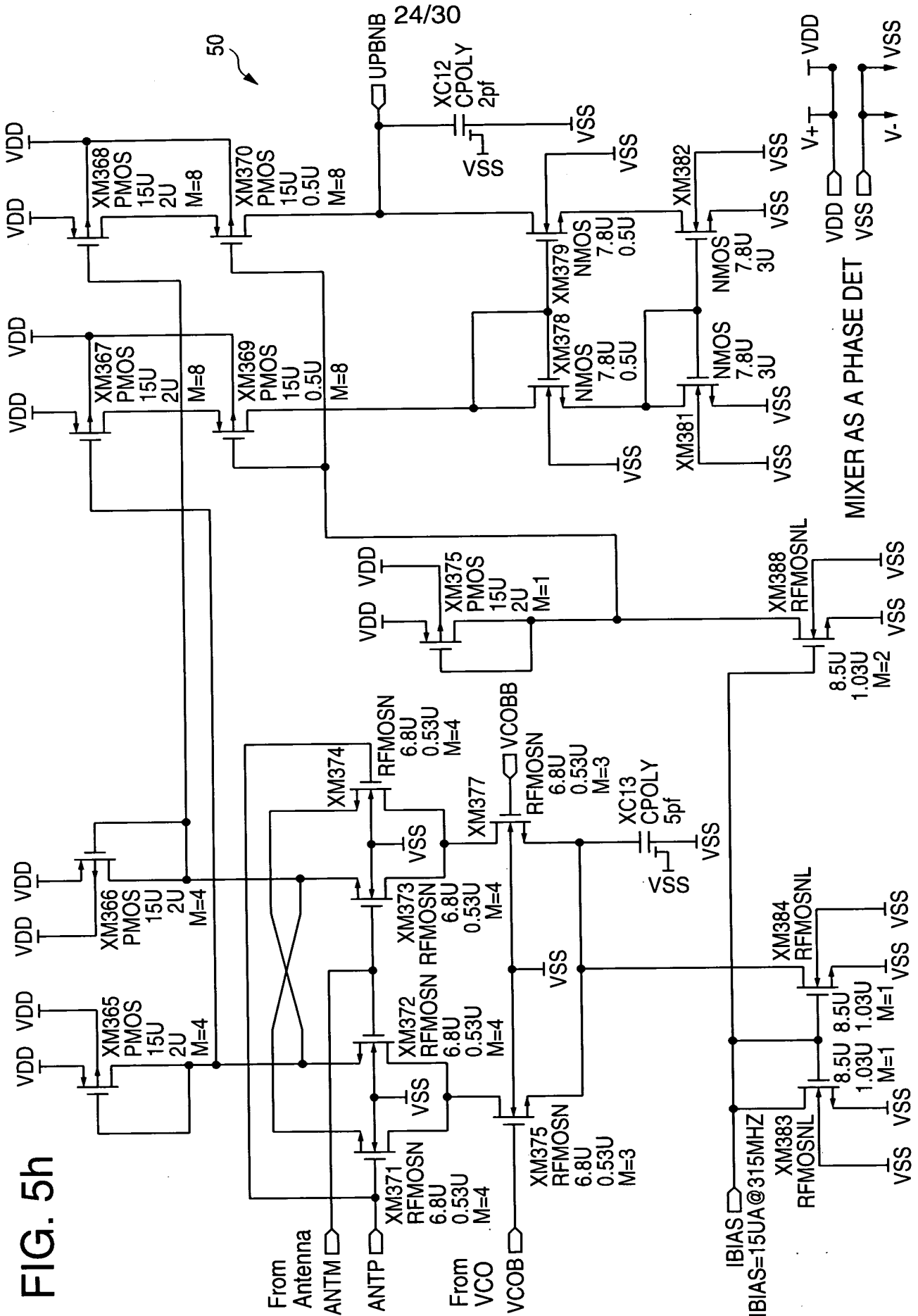


FIG. 5g

FIG. 5g-1

FIG. 5g-2

FIG. 5h



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Gain (sin) = 5V per 100ns for 90 deg
Linearity (max) = 200MV
IBIAS = IPOLY

FIG. 5i-1

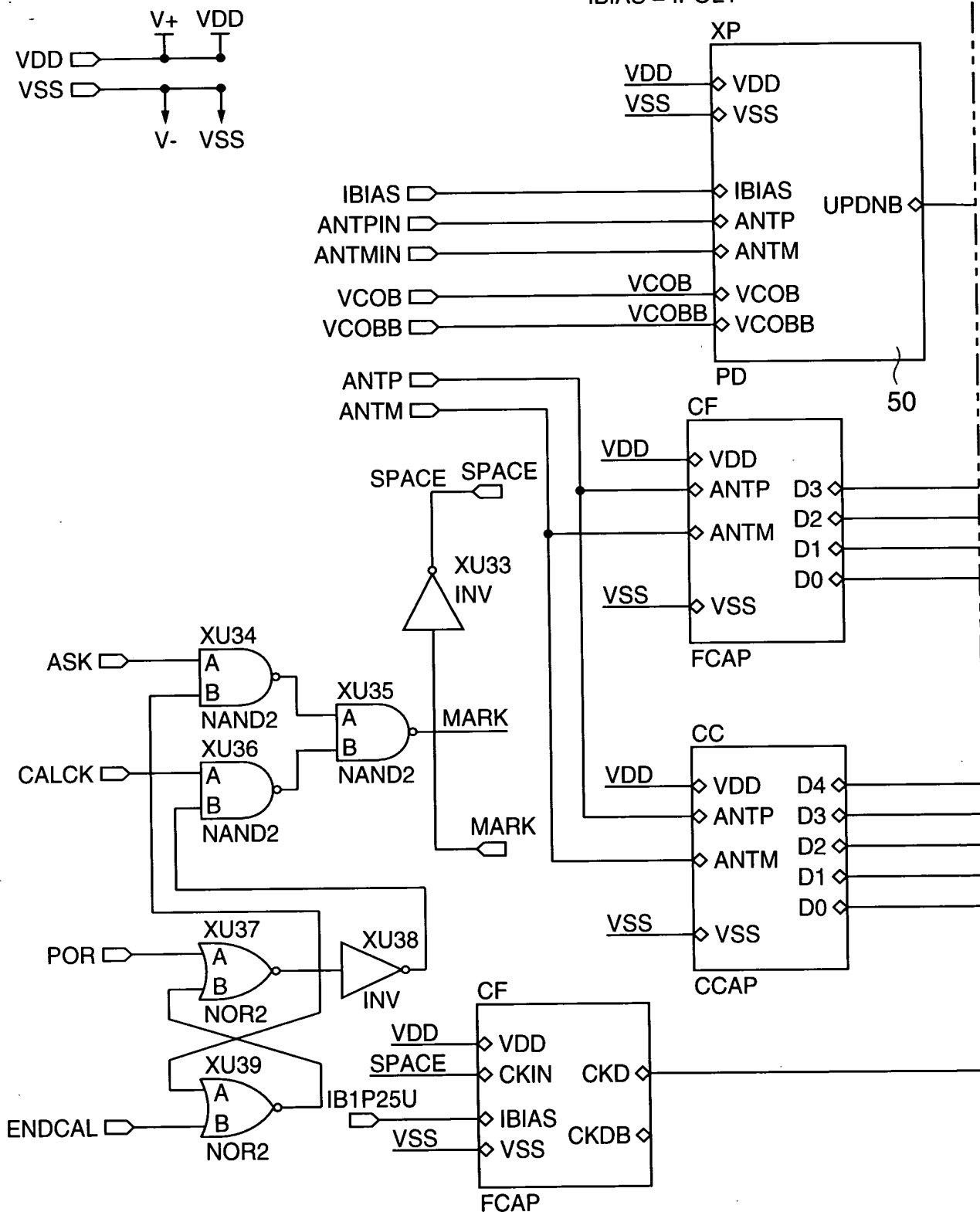
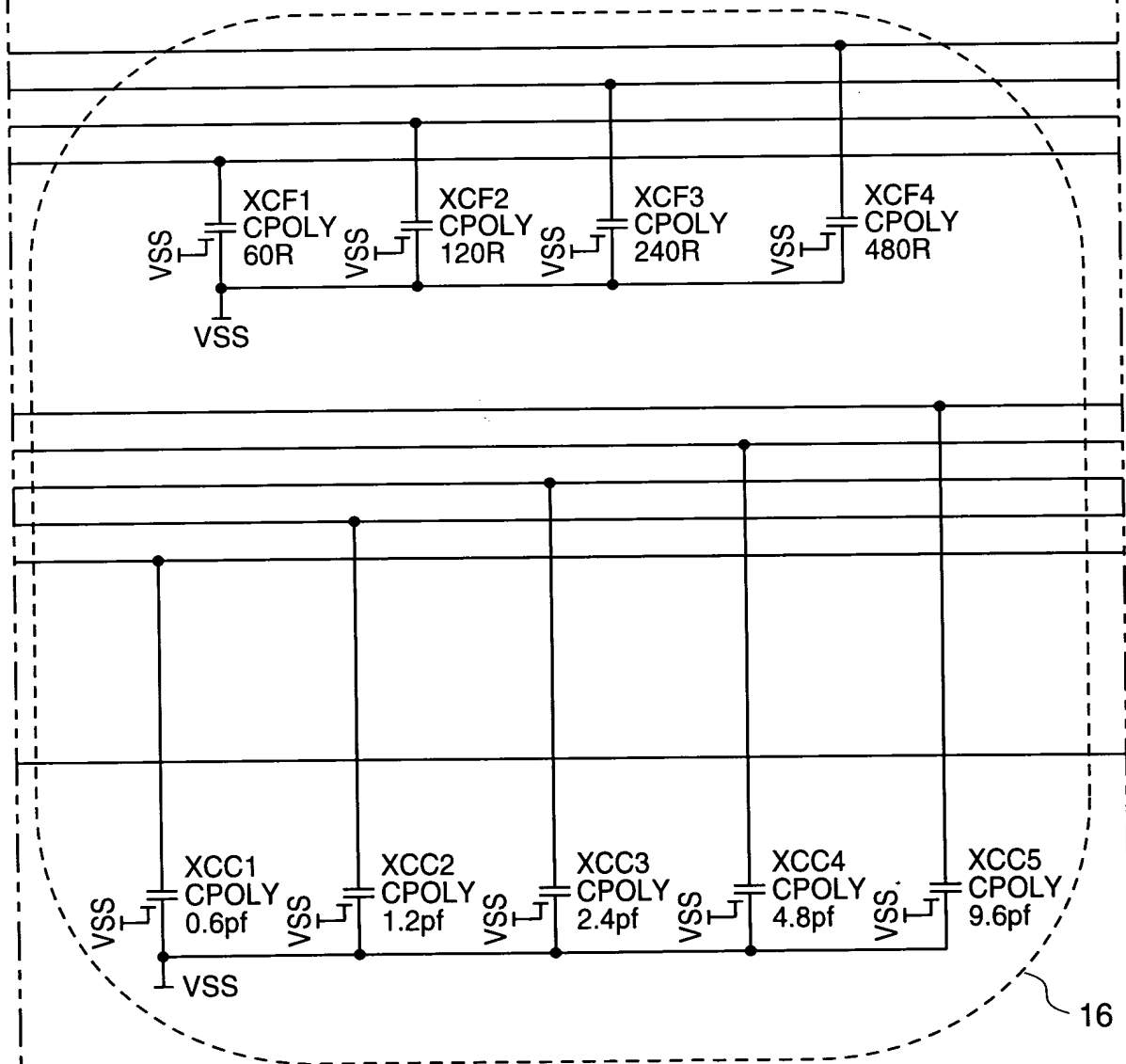


FIG. 5i-2

[illegible]

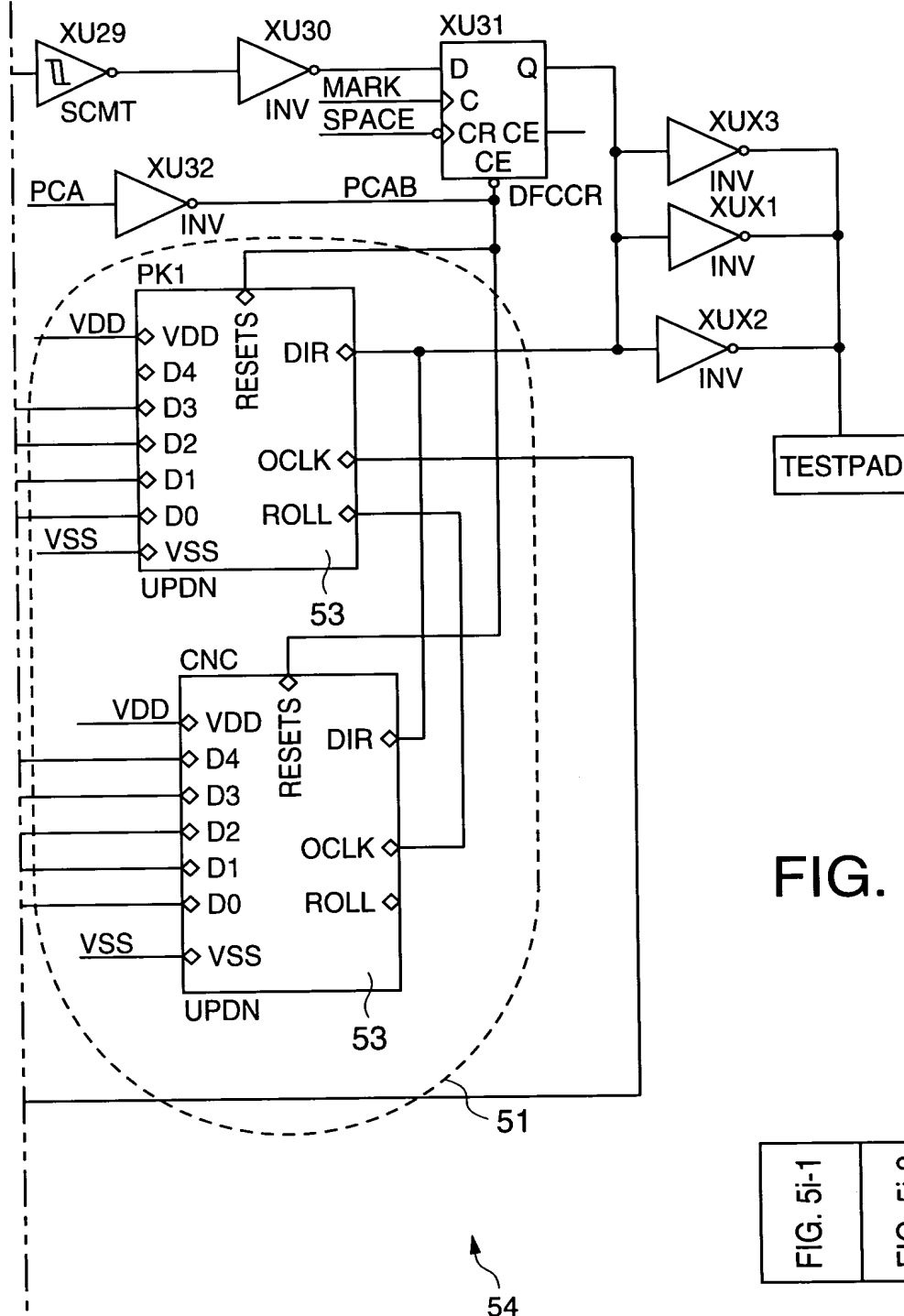


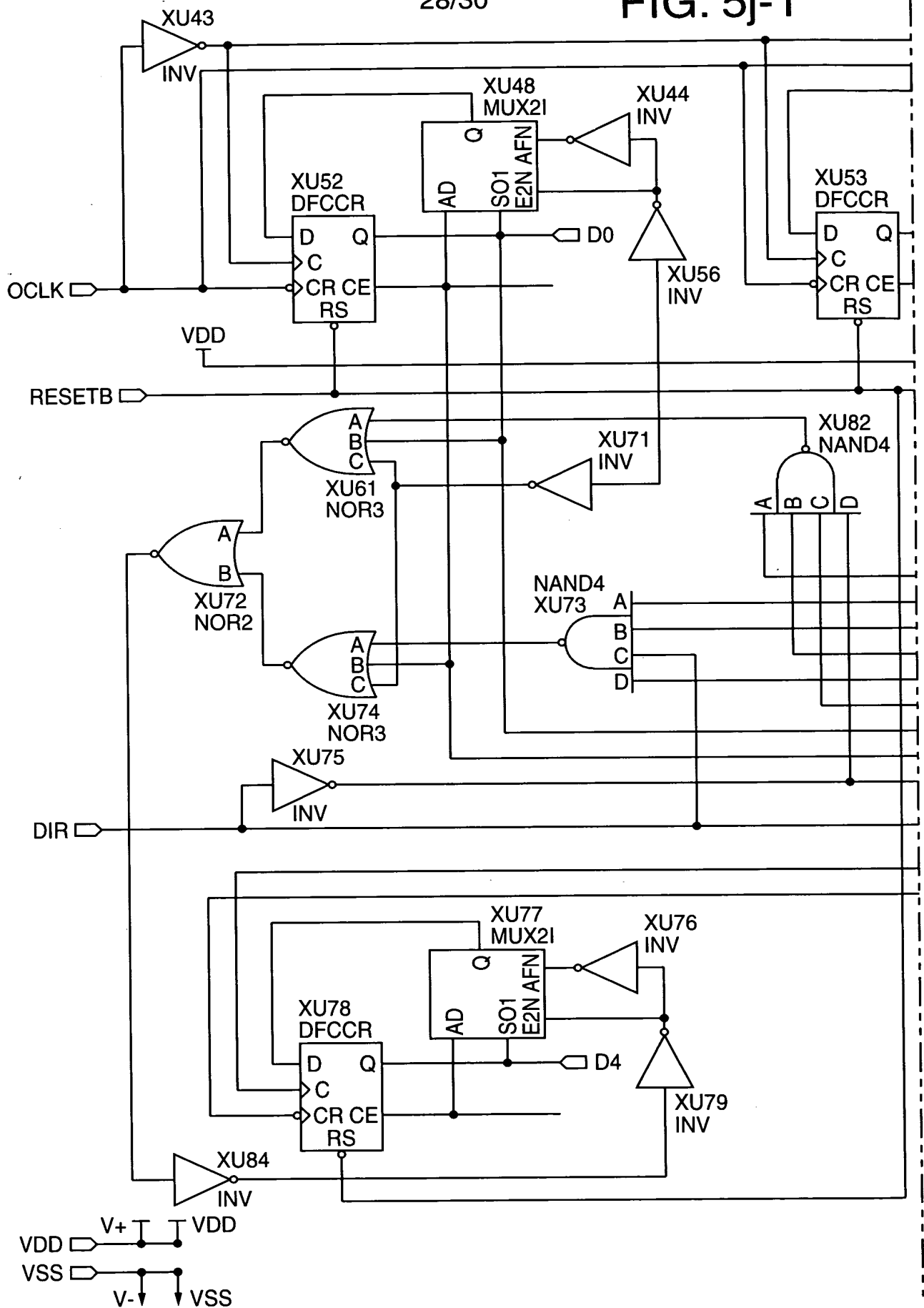
FIG. 5i-3

FIG. 5i-1	FIG. 5i-2	FIG. 5i-3
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FIG. 5i

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FIG. 5j-1



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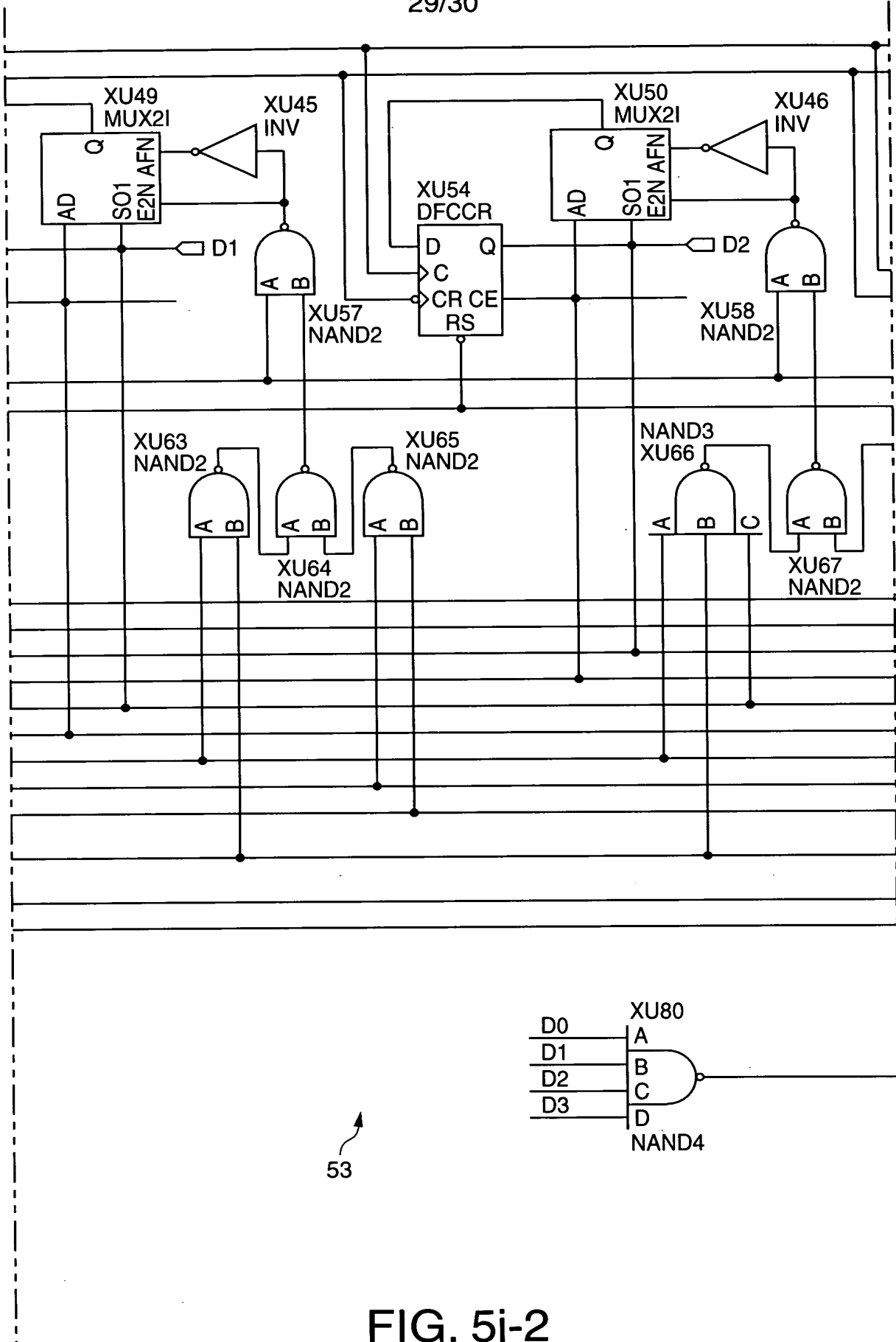


FIG. 5j-2

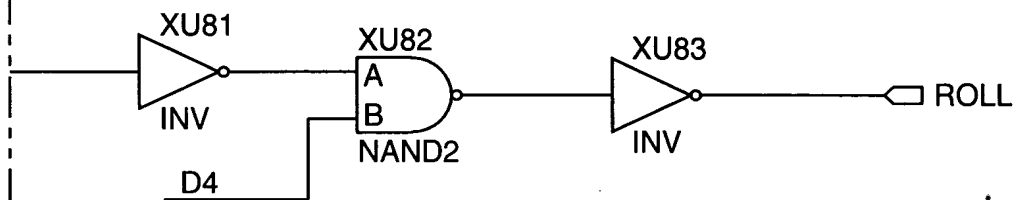
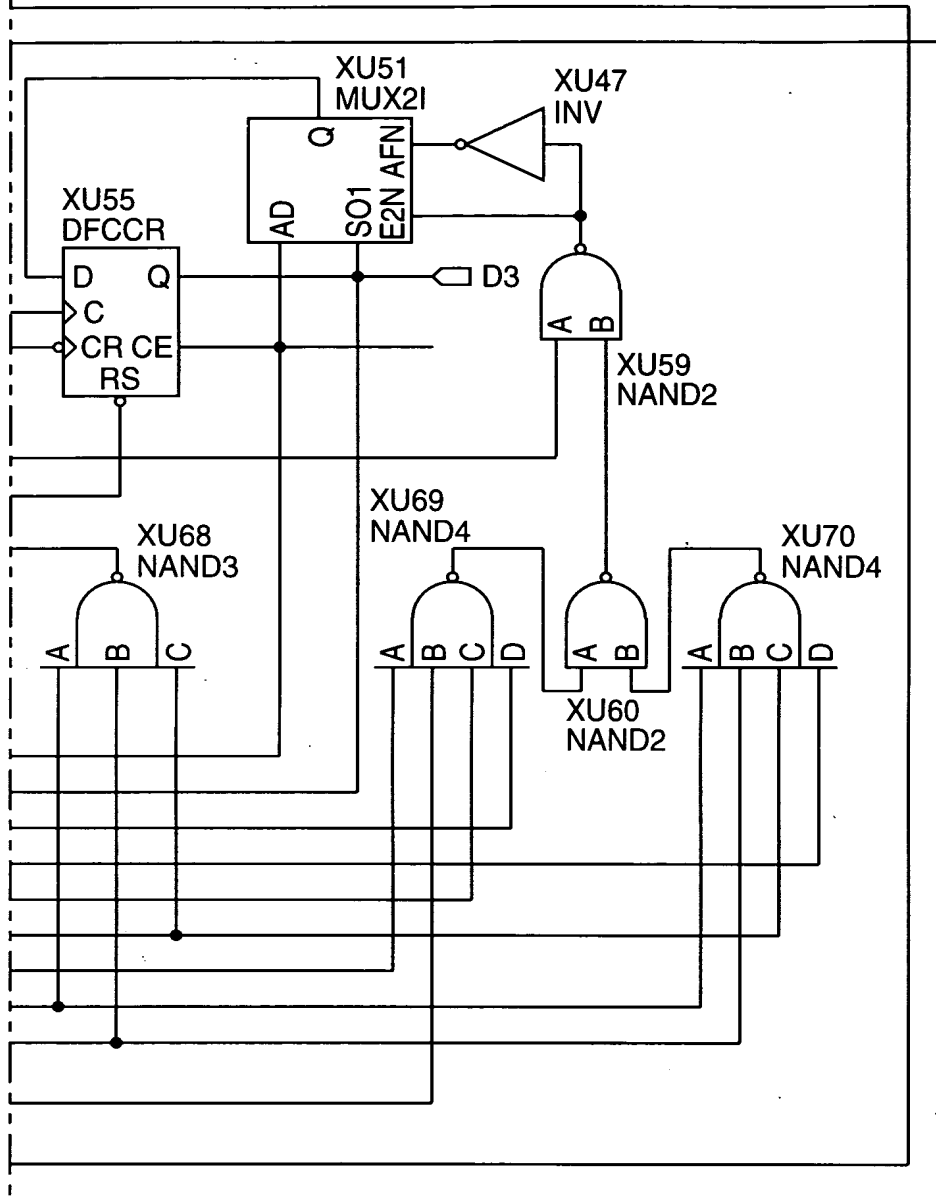


FIG. 5j-3

FIG. 5j

FIG. 5j-1

FIG. 5j-2

FIG. 5j-3